



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

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Serial No.: 10/748,559

Filed: December 29, 2003

For: A CMOS DEVICE WITH  
METAL AND SILICIDE GATE  
ELECTRODES AND A  
METHOD FOR MAKING IT

Art Unit: 2814

Examiner: T. Le

Attorney Docket: P18244

Commissioner for Patents  
P.O. Box 1450  
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**DECLARATION OF MARK V. SEELEY**

Dear Sir:

I, Mark V. Seeley, declare that:

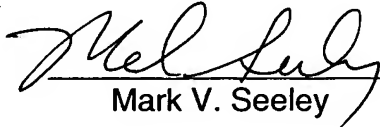
1. I am the patent attorney who drafted the captioned non-provisional patent application, which is assigned to Intel Corporation.
2. I am familiar with the application that recently issued as United States Patent No. 6,858,483 ("Doczy et al"), having submitted the Doczy et al. patent application with an information disclosure statement that accompanied the captioned application. The Doczy et al. patent application and the pending patent application were, at the time the

invention claimed in the pending patent application was made, subject to an obligation of assignment to the same entity – namely Intel Corporation.

3. This declaration is being submitted to support a response to an Office Action mailed on March 3, 2005 for U.S. Patent Application Serial Number 10/748,559 entitled "A CMOS Device with Metal and Silicide Gate Electrodes and a Method for Making it."

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the captioned application or any patent issued thereon.

Date: March 28, 2005

  
Mark V. Seeley